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APPLICANT(S) : Y. Chikama, et al

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## UNITED STATES PATENT DOCUMENTS

EXAM. INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FIL. DATE IF APPR

## FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRAN YES/NO
<i>W/kt</i>	BA /	4-232922	8/21/92	Japan			See English Abstract
	BB /	2-83533	3/23/90	Japan			See English Abstract
	BC /	2-223924	9/6/90	Japan			See English Abstract
	BD /	1-96383	4/14/89	Japan			See English Abstract
	BE	10-245444	9/14/98	Japan			See English Abstract
	BF /	11-170421	6/29/99	Japan			See English Abstract
	BG	62-288883	12/15/87	Japan			See Eng. Partial Transla- tion

## OTHER DOCUMENTS (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)

<i>W/kt</i>	CA	M. Ikeda, et al. "Low Resistance Copper Address Line for TFT-LCD". Japan Display '89, pp. 498-501. 1989
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Examiner: *Spurta Akemi*

Date: 5/6/02